

# FYP Offer for Batch: 2009-10

FRPAPA is a research project at Electronics Design Centre (EDC) jointly funded by NEDUET and ICT R&D Fund. As part of this project, EDC is offering FYP opportunities in the domain of Analog and RFIC design for BE students belonging to the Faculty of ECE

## What we offer?

- Stipend + tuition fee of selected meritorious candidates (up to Rs.7k per month)
  - Work on state-of-the-art design tools
  - Professional, friendly and open work environment
    - Possible hiring after graduation
- Recommendation letter & help in securing admission for studies abroad
- Ideal opportunity for those who want to pursue R&D career in this domain

## Whom are we looking for?

- Bright & hardworking individuals with strong academic credentials especially in Circuit Theory and Electronics
  - Team player with result oriented approach
  - Excellent analytical and communication skills
- Knowledge of RFIC and/or analog circuit design will be a plus

## Interested?

Send your CV and scanned copies of all university marksheets to [edc@neduet.edu.pk](mailto:edc@neduet.edu.pk) with subject: "FYP for Batch: 2009-10" latest by **September 9, 2012**

For details about EDC, please visit:

<http://www.neduet.edu.pk/electronics/edc/index.html>