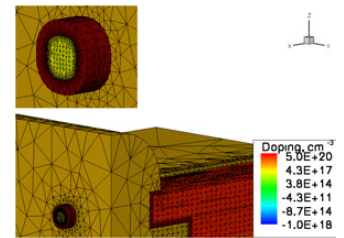


Attention T.E (Electronics)

Batch 2010- 2011



Final Year Projects (FYP – 2014)

Offered by **Electronics Design Center**
(Dept. of Electronic Engineering)

Applications are invited from the regular students of Third Year Electronic Engg.(Batch 2010-2011) for Final year Projects (FYP-2014). Offered projects are in the area of **Semiconductor Devices, Physical modeling and Simulations on TCAD.**

Detailed CV and scanned copies of previous Marks Sheets (upto 5th semester) can be emailed at the email address : edc@neduet.edu.pk
(your CV must contain valid email address and telephone number)

Selection Criteria: Short listed students will be called for test and interview. Test will be based on the pre-requisites mentioned below

Pre-Requisites: (You must have...)

1. Good Knowledge of Basic Electronics and Semiconductor Physics.
2. Basic know how of C-language programming
3. Good skills of searching and researching on the web (Google etc.)
4. Good communication skills
5. Very Good presentation skills(Personal, verbal and power point/prezi)

Last date for applying: **December 17th, 2013**

Contact Us: edc@neduet.edu.pk

Dr. Sadia Muniza Faraz